

10/073546

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jenny M. Pelner

Examiner: Duncan, Marc

Patent No.: 7,010,723

Group Art Unit: 2113

Issue Date: March 7, 2006

Docket No: 884.616US1

Title: METHODS AND APPARATUS TO TEST COMPUTER SYSTEM COMPONENTS (as amended)

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

ATTN: CERTIFICATE OF CORRECTION BRANCH

Certificate

APR 0.4 2006

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 \underline{X} A check in the amount of \$100.00 to cover the Certificate of Correction fee.

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- X Certificate of Correction Form PTO-1050 (in duplicate)
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Customer No: 21186

Name: Ann M. McCrackin

Reg. No.42,858

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Chris Hannord

Signature



PATENT

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.:

7,010,723

Docket No: 884.616US1

Issue Date:

March 7, 2006

Patentee: Jenny M. Pelner

Customer No.: 21186

Confirmation No.: 5100

Title

METHODS AND APPARATUS TO TEST COMPUTER SYSTEM COMPONENTS (as

amended)

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Pursuant to 1.20(a), enclosed please find a check in the amount of \$100.00.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted

04/03/2006 MWOLDGE1 00000058 7010723

JENNY M. PELNER

01 FC:1811

100.00 OP

By his Representatives,

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Date: March 29, 200 lo

By: Ann M. McCrackin

Reg. No: 42,858

AMM1:sj

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

: 7,010,723

Page (1) of 1

DATED

: March 7, 2006

INVENTOR(S)

: Pelner

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (54), in "Title", in column 1, lines 1-3, delete "METHOD TO COUPLE INTEGRATED CIRCUIT PACKAGES TO BONDING PADS HAVING VIAS" and insert - - METHODS AND APPARATUS TO TEST COMPUTER SYSTEM COMPONENTS - -, therefor.

In column 1, lines 1-3, delete "METHOD TO COUPLE INTEGRATED CIRCUIT PACKAGES TO BONDING PADS HAVING VIAS" and insert - - METHODS AND APPARATUS TO TEST COMPUTER SYSTEM COMPONENTS - -, therefor.

MAILING ADDRESS OF SENDER:

PATENT NO. _____7,010,723____

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